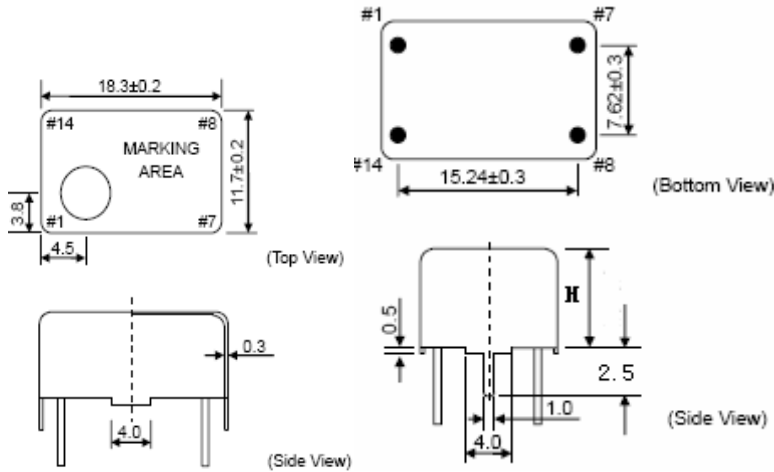


Series

TCXO



External dimension



PIN	CONNETCTION
1	VC
7	GND
8	OUTPUT
14	+V _{CC}
SERIER	H
TC100	7.7MAX
TC200	5.9MAX

Item		Specification		Remsrks	
Series		TC100	TC200		
Nominal Frequency Range		8.000MHz to 40.000MHz		Fundamental Mode:AT Cut	
		30.000MHz to 150.000MHz		PLL	
Frequency Stability Over Temperature Range		± 1.0 PPM		0°C to 50°C	
		± 1.5 PPM		-10°C to +60°C	
		± 2.0 PPM		-20°C to +70°C	
		± 2.5 PPM		-30°C to +750°C	
		± 5.0 PPM		-40°C to +85°C	
Supply Voltage		+3.3V _{DC} $\pm 5\%$ or +5.0V _{DC} $\pm 5\%$			
Supply Current		3mA		Clipped Sine	
		25mA		TTL/CMOS	
Frequency Stability	Aging	± 1.0 PPM		Per Year	
	Versus Supply Variation	± 0.3 PPM		Per V _{CC} $\pm 5\%$	
	Versus Out Load Variation	± 0.2 PPM		Per Standard Load $\pm 10\%$	
	Jitter	100ps Peak to Peak			
	Short-term Frequency Stability	Allan Variation	$1 \cdot 10^{-8}$ /s		
Phase Noise			100HZ	-100dBc/Hz	
			1KHZ	-130dBc/Hz	
			10KHZ	-140dBc/Hz	
	100KHZ	-145dBc/Hz			
Output and Load	Output Waveform	Clipped Sine	TTL/CMOS		
	Output Levels	1 V _{p-p}	TTL/CMOS		
	Duty Cycle	50% $\pm 10\%$			
	Rise Time & Fall Time	5ns			
	Load	10K Ω //10pF	2LSTTL/CMOS		
Frequency Adjustment		± 3 PPM			

Remark:

All specifications subject to change without not notice.